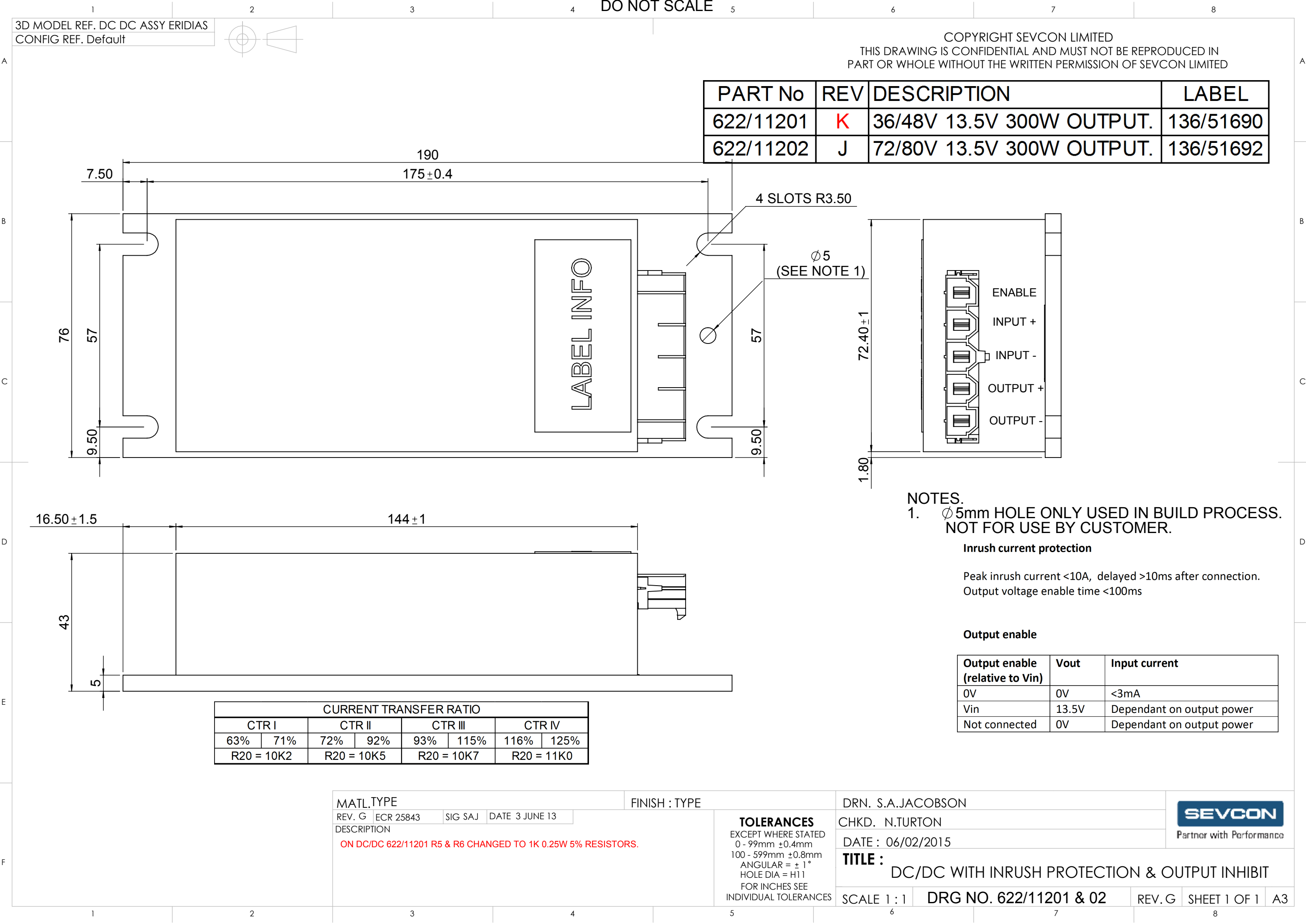




3D MODEL REF. DC DC ASSY ERIDIAS
CONFIG REF. Default



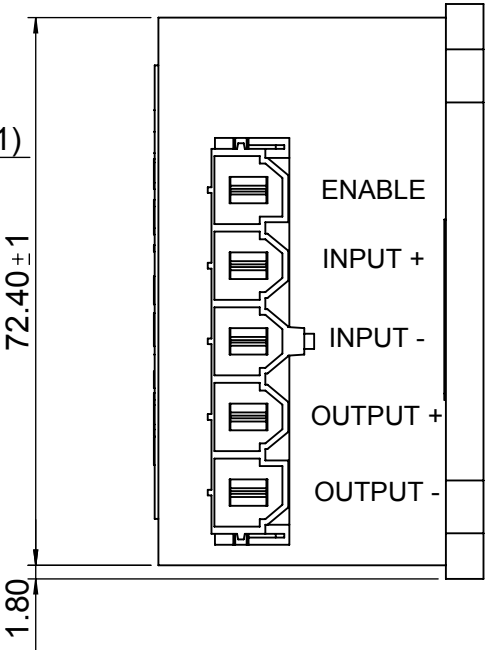
DO NOT SCALE

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PART No	REV	DESCRIPTION	LABEL
622/11201	K	36/48V 13.5V 300W OUTPUT.	136/51690
622/11202	J	72/80V 13.5V 300W OUTPUT.	136/51692

4 SLOTS R3.50

Ø 5
(SEE NOTE 1)



NOTES.
1. Ø 5mm HOLE ONLY USED IN BUILD PROCESS.
NOT FOR USE BY CUSTOMER.

Inrush current protection

Peak inrush current <10A, delayed >10ms after connection.
Output voltage enable time <100ms

Output enable

Output enable (relative to Vin)	Vout	Input current
0V	0V	<3mA
Vin	13.5V	Dependant on output power
Not connected	0V	Dependant on output power

CURRENT TRANSFER RATIO							
CTR I		CTR II		CTR III		CTR IV	
63%	71%	72%	92%	93%	115%	116%	125%
R20 = 10K2		R20 = 10K5		R20 = 10K7		R20 = 11K0	

MATL. TYPE				FINISH : TYPE		DRN. S.A.JACOBSON		SEVCON Partner with Performance	
REV. G	ECR 25843	SIG SAJ	DATE 3 JUNE 13			CHKD. N.TURTON			
DESCRIPTION					TOLERANCES EXCEPT WHERE STATED 0 - 99mm ±0.4mm 100 - 599mm ±0.8mm ANGULAR = ± 1° HOLE DIA = H11 FOR INCHES SEE INDIVIDUAL TOLERANCES	DATE : 06/02/2015		TITLE : DC/DC WITH INRUSH PROTECTION & OUTPUT INHIBIT	
ON DC/DC 622/11201 R5 & R6 CHANGED TO 1K 0.25W 5% RESISTORS.									
						SCALE 1 : 1		DRG NO. 622/11201 & 02	
								REV. G	SHEET 1 OF 1